Please amend the specification by replacing paragraphs as follows:

A. Specification Paragraphs With Mark-ups to Show Changes Made

The following are mark-ups to show changes made to paragraph(s) starting at page 2, line 23 and ending at page 3, line 6:

First, in the stripe type barrier structure, as shown in Fig. 2a, a plurality of first substrate electrode pairs consisting of Y electrode 11 and Z electrode 12 are formed in a row direction at constant intervals. Stripe type barriers 13 are formed across the first substrate electrode pairs at constant intervals. An X electrode(not shown) is formed in a central portion between the respective barriers. A reference numeral 21 [which is not described] denotes a discharge region and a reference numeral 22 denotes a main discharge region.

The following are mark-ups to show changes made to paragraph(s) starting at page 3, line 20 and ending at page 4, line 2:

As described above, in the stripe type barrier structure, lower sides of the barriers 13 are located at a distance away from the main discharge region [22] 18. Thus, the distance between the main discharge region [22] 18 and the phosphor layer [18] 17 below the barriers 13 is farther than the distance between the main discharge region [22] 18 and the phosphor layer [18] 17 above the X electrode [11] 14. For this reason, loss occurs [while] when ultraviolet rays generated by discharge reach a portion below the barriers.

The following are mark-ups to show changes made to paragraph(s) starting at page 10, line 8 and ending at page 11, line 1:

[Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings.]

- First Embodiment -

As shown in Figs. 4a and 4b, a plasma display panel according to [the] a first embodiment of the present invention includes first substrate electrode pairs (sustain electrodes) [consisting of] with a Y electrode 41 and a Z electrode 42, formed on a first substrate in one direction, first barriers 43 formed on a second substrate at constant intervals to cross the first substrate electrode pairs, auxiliary barriers 43a formed [at] on both sides of the first barriers 43, and second barriers 43b formed on the second substrate corresponding to a region between the respective first substrate electrode pairs. The second barriers 43b have a width which increases toward the first barriers 43 and are symmetrical toward the first substrate electrode pairs at their adjacent both sides. A reference numeral 51 [which is not described] denotes a main discharge region.

The following are mark-ups to show changes made to paragraph(s) starting at page 11, line 14 and ending at page 11, line 17:

A reference numeral 40 [which is not described] denotes the first substrate, 40a denotes the second substrate, 44 denotes an X electrode, 45 and 46 denote dielectric layers, and 47 denotes a phosphor layer.

The following are mark-ups to show changes made to paragraph(s) starting at page 13, line 7 and ending at page 13, line 17:

Stripe type barriers 121 on which a phosphor layer 123 is deposited are formed on the second substrate 120. A plurality of projections 130 are formed between the barriers 121 at constant intervals. At this time, the projections 130 are preferably arranged between the barriers [21] 121 corresponding to the boundary portion between the cells. A top portion at both sides of the projections 130 should be separated from inner sides of the barriers [21] 121. It is preferable that both sides of a lower portion of the projections 130 are separated from the inner sides of the barriers 121. However, both sides of the lower portion of the projections 130 may be mounted in the inner sides of the barriers 121.

The following are mark-ups to show changes made to paragraph(s) starting at page 17, line 19 and ending at page 18, line 6:

As shown in Fig. 15, a plasma display panel according to the fourth embodiment of the present invention includes first substrate electrode pairs 341 and 342 formed on a first substrate in one direction, second substrate electrodes (address electrodes) 343 formed to cross the first substrate electrode pairs 341 and 342, first barriers 344 formed at both sides with a cell region interposed therebetween, the cell region being defined on a region where the first substrate electrode pairs 341 and 342 cross the second substrate electrodes 343, and a plurality of second barriers 344a formed in upper and lower sides of the cell region with the cell region interposed therebetween to be separated from the first barriers 344. A reference numeral 345 [which is not described] denotes a main discharge region.

B. Clean Specification Changes

Please replace paragraph(s) starting at page 2, line 23 and ending at page 3, line 6 with the following paragraph(s):

First, in the stripe type barrier structure, as shown in Fig. 2a, a plurality of first substrate electrode pairs consisting of Y electrode 11 and Z electrode 12 are formed in a row direction at constant intervals. Stripe type barriers 13 are formed across the first substrate electrode pairs at constant intervals. An X electrode(not shown) is formed in a central portion between the respective barriers. A reference numeral 21 denotes a discharge region and a reference numeral 22 denotes a main discharge region.

Please replace paragraph(s) starting at page 3, line 20 and ending at page 4, line 2 with the following paragraph(s):

As described above, in the stripe type barrier structure, lower sides of the barriers 13 are located at a distance away from the main discharge region 18. Thus, the distance between the main discharge region 18 and the phosphor layer 17 below the barriers 13 is farther than the distance between the main discharge region 18 and the phosphor layer 17 above the X electrode 14. For this reason, loss occurs when ultraviolet rays generated by discharge reach a portion below the barriers.

Please replace paragraph(s) starting at page 10, line 8 and ending at page 11, line 1 with the following paragraph(s):

- First Embodiment -

As shown in Figs. 4a and 4b, a plasma display panel according to a first embodiment of the present invention includes first substrate electrode pairs (sustain electrodes) with a Y electrode 41 and a Z electrode 42, formed on a first substrate in one direction, first barriers 43 formed on a second substrate at constant intervals to cross the first substrate electrode pairs, auxiliary barriers 43a formed on both sides of the first barriers 43, and second barriers 43b formed on the second substrate corresponding to a region between the respective first substrate electrode pairs. The second barriers 43b have a width which increases toward the first barriers 43 and are symmetrical toward the first substrate electrode pairs at their adjacent both sides. A reference numeral 51 denotes a main discharge region.

Please replace paragraph(s) starting at page 11, line 14 and ending at page 11, line 17 with the following paragraph(s):

A reference numeral 40 denotes the first substrate, 40a denotes the second substrate, 44 denotes an X electrode, 45 and 46 denote dielectric layers, and 47 denotes a phosphor layer.

Please replace paragraph(s) starting at page 13, line 7 and ending at page 13, line 17 with the following paragraph(s):

Stripe type barriers 121 on which a phosphor layer 123 is deposited are formed on the second substrate 120. A plurality of projections 130 are formed between the barriers 121 at constant intervals. At this time, the projections 130 are preferably arranged between the barriers 121 corresponding to the boundary portion between the cells. A top portion at both sides of the projections 130 should be separated from inner sides of the barriers 121. It is preferable that both sides of a lower portion of the projections 130 are separated from the inner sides of the barriers 121. However, both sides of the lower portion of the projections 130 may be mounted in the inner sides of the barriers 121.

Please replace paragraph(s) starting at page 17, line 19 and ending at page 18, line 6 with the following paragraph(s):

As shown in Fig. 15, a plasma display panel according to the fourth embodiment of the present invention includes first substrate electrode pairs 341 and 342 formed on a first substrate in one direction, second substrate electrodes (address electrodes) 343 formed to cross the first substrate electrode pairs 341 and 342, first barriers 344 formed at both sides with a cell region interposed therebetween, the cell region being defined on a region where the first substrate electrode pairs 341 and 342 cross the second substrate electrodes 343, and a plurality of second barriers 344a formed in upper and lower sides of the cell region with the cell region interposed

therebetween to be separated from the first barriers 344. A reference numeral 345 denotes a main discharge region.